**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Serial No .:

9/434.082

Kevin J. Ryan

09/434,082

Examiner: James Peikari

Group Art Unit: 2186

Filed:

November 5, 1999

Docket: 303.306US2

Title:

PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A

UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A

**BIDIRECTIONAL DATA BUS** 

RECEIVED

## **INFORMATION DISCLOSURE STATEMENT**

DEC 2 7 2002

**Assistant Commissioner for Patents** Washington, D.C. 20231

Technology Center 2100

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Account No. 19-0743 in order to have this Information Disclosure Statement considered.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KEVIN J. RYAN

By his Representatives,

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Date 16 Dec 02

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this <a href="Lucation-number-10">Lucation-number-10</a> day of December, 2002.

Name

Substitute for form 1449A/PTO INFORMATION DISCLOSURE SPAPEMENT BY APPLICANT (Use as many states as necessary) 2 3 2002

Sheet 1 of 1

<b>Application Number</b>	09/434082	
Filing Date	November 5, 19	99
First Named Inventor	Ryan, Kevin	RECENT
Group Art Unit	2186	TILOLIVE
Examiner Name	Peikari, James	DEC 2 7 2002

Attorney Docket No: 303.306US2

Technology Center 2100

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,270,964	12/14/1993	Bechtolsheim, A., et al	365	52	05/19/1992
	US-5,598,545	01/28/1997	Childers, J., et al	395	562	03/25/1993
	US-5,625,780	04/29/1997	Hsieh, W., et al	395	311	11/02/1994
	US-5,721,862	02/01/1998	Sartore, R., et al	711	118	06/02/1995
	US-5,903,509	05/11/1999	Ryan, K., et al	365	230.03	05/04/1998
	US-6,172,893	01/09/2001	Ryan, K.	365	49	01/05/1999

		<b>FOREIGN PATENT</b>	DOCUMENTS			
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	-	"400 Mb/s/pin SLDRAM, 4M x 18 SLDRAM, Pipelined, Eight Bank, 2.5V	
		Operation", SLDRAM Inc., Draft/Advance, SLD4M18DR400, 4 MEG x 18	
		SLDRAM,(1998),pp. 1-69	
		"Draft Standard for A High-Speed Memory Interface (SyncLink)", The Institute of	
		Electrical and Electronics Engineers, Inc., Draft 0.99 IEEE P1596.7-	
		199X,(1996),pp. 1-55	<u>L</u> .
		"New NEC Memory Technology Increases Performance by 20%",	
		http://www.nbpacifica.com/headline/newnecmemorytechnolo_1144.sh,(1997),pp.	
		1-2	<u> </u>
		"Virtual Channel Memory", NEC Electronics Inc., (1997),pp. 1-2	
		GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", The	
		SLDRAM Consortium, (Aug. 1997),pp. 1-14	

**DATE CONSIDERED EXAMINER**